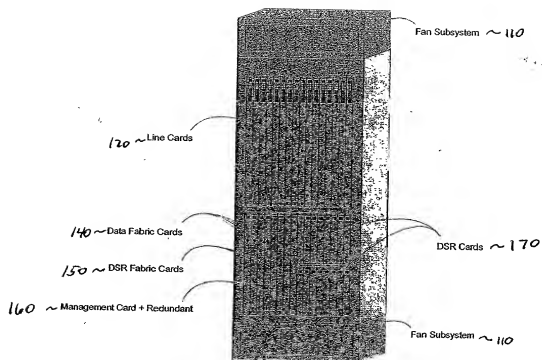


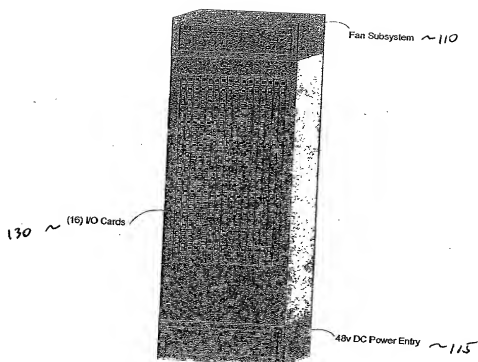
09774016.062601



100

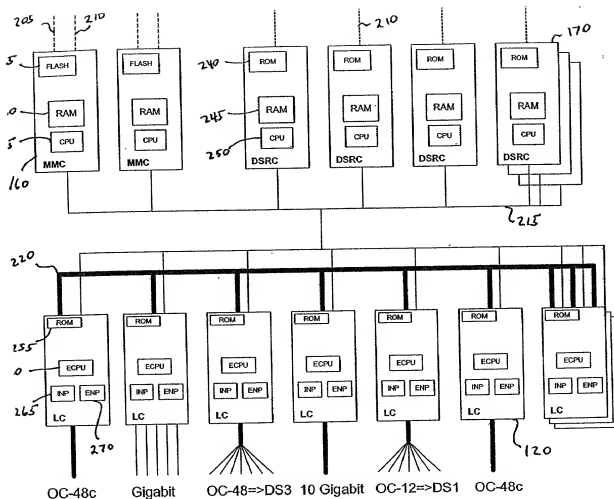
FIG 1A

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Fig 1 B



MMC - Management Module Card
 DSRC - Distributed Service Router Card
 CPU - the main CPU for DSRC, could be a PowerPC
 LC - Line Card
 FLASH - to hold image and configuration files, serve as Booter as well.
 ROM - BootROM
 ECPU - the Exception Processor, could be the same as the CPU for DSRC
 INP - the Ingress Network Processor for packet processing, including co-processor and custom ASICs
 ENP - the Egress Network Processor

----- the console port
 ----- the 10/100M Ethernet port for out of band management
 ----- the line bus or lower speed fabric connecting between the SPRC and DSRC, and between the Line Cards and DSRC/SPRC. This is mainly for ICM.
 ===== the high speed fabric for the switch traffic

FIG. 2

FIG. 3

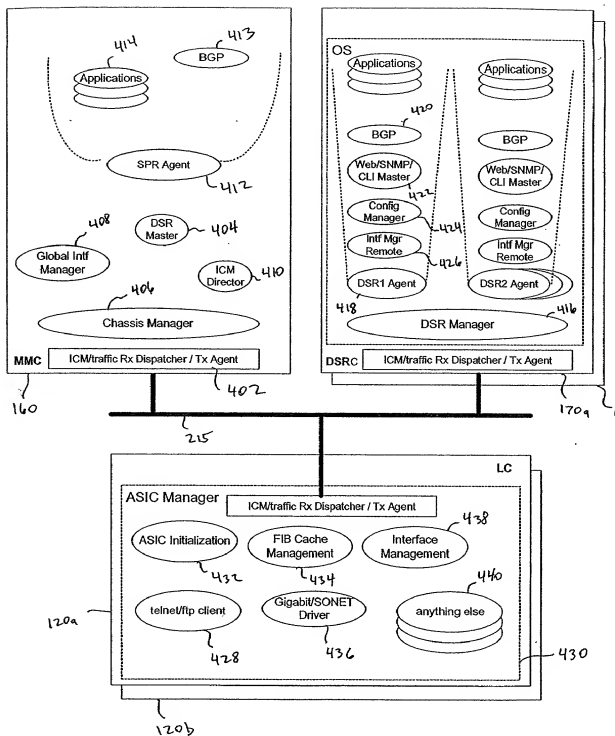


FIG. 4

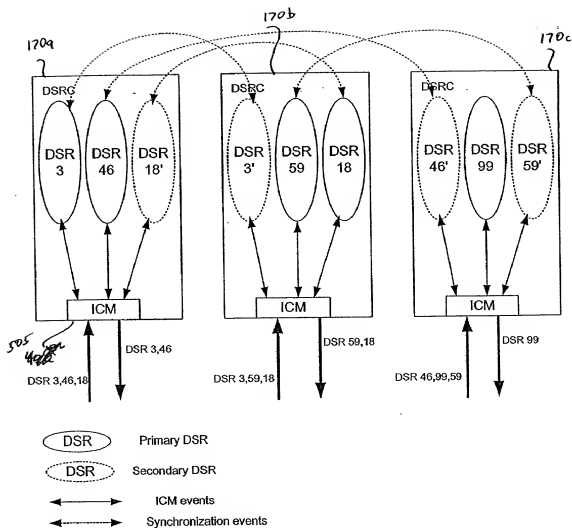


FIG 5